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ABSTRACT

5 The present invention relates to a method and an arrangement for a testing of substrates (1) provided with a predetermined pattern, in particular circuit boards with an application of solder paste. In accordance with
10 the present invention the actual pattern (1a) applied to the substrate by means of a printing or structuring process (3, 4) is optically detected, the optically detected actual pattern is compared with a desired pattern and in dependence on the comparison and taking
15 into account permissible tolerances it is determined to which further process the observed substrate provided with the actual pattern is to be delivered, wherein the optical detection of the actual pattern is effected in the form of digital data with the formation of an actual
20 data set, a desired data set is formatted from control data for the application of the pattern to the substrates, and data processing is carried out to the effect that the desired data set and the actual data set are compared datawise with one another taking into
25 account permissible tolerances.

[Fig. 1]